



## BBP

# BASE BAND PROCESSOR

Model No. ICS-ECIL-BBPM.V01

*The Base band Processor (BBP) is a Xilinx FPGA based solution for voice and data modulation and demodulation at 455KHz IF*

### System Specification

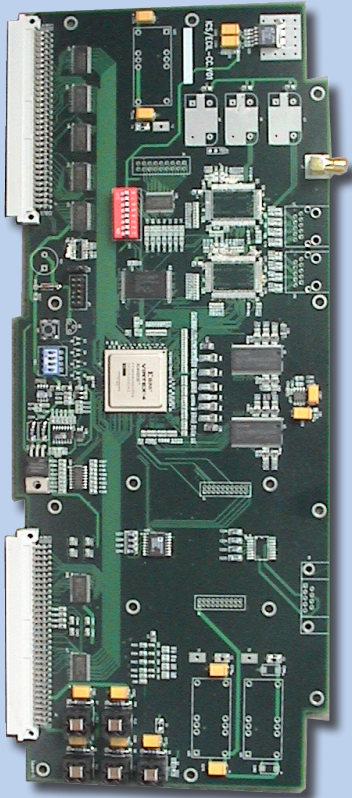
Channel Spacing	: 25KHz
Classes of emission	: AM : A3E,A9E
Type of operation	: Simplex and half duplex
Modes of operation	: Clear voice and data, secure voice and data

### Receiver specifications

IF Frequency	: 455KHz
Impedance	: 50ohms
Level sensitivity	: -30 dBm to -10 dBm
AM internal noise level	: (S+N)/N $\geq$ 40dB
FM noise quieting	: (S+N)/N $\geq$ 40dB

### Selectivity (IF Band width)

BW1(25KHz channel spacing) Squelch	: $\geq$ 24KHz/6dB, 50KHz.60dB
SINAD setting range	: 10dB to 12dB
Serial Interfaces	: I 2C for system interface, RS232C and RS422 interface
IF Connectors	: 2, SMA type Connectors
Interface Connectors	: 2, 96 pin EURO Connectors
DC Operation	: +20V,+8V, -8V, +5V



NOTE : The specifications can be changed without notice due to technological advances.